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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Terunao HANAOKA

Group Art Unit: 2822

Application No.: 10/679,467

Examiner: T. TRAN

Filed: October 7, 2003

Docket No.: 117462

For: SEMICONDUCTOR WAFER, SEMICONDUCTOR DEVICE AND METHOD OF
MANUFACTURING THE SAME, CIRCUIT BOARD, AND ELECTRONIC
EQUIPMENT

AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In reply to the April 21, 2006 Office Action, please consider the following:

Amendments to the Claims as reflected in the listing of claims; and

Remarks.